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## \*BIBDATASHEET\*

CONFIRMATION NO. 6522

Bib Data Sheet

SERIAL NUMBER 10/748,025	FILING DATE 12/30/2003  RULE	CLASS 428	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. 20059/PIA31072
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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

(None) LT

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

REPUBLIC OF KOREA 10-2003-0086391 12/30/2003

LT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/28/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 7	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <u>Chenman</u> Initials <u>LT</u>				

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TITLE

Methods of manufacturing transistors using dummy gate patterns

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